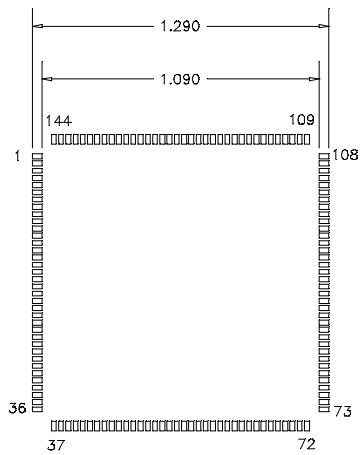


REV. B F1360

RECOMMENDED MINIMUM PAD LAYOUT



144 143 142 141 138 136 134 132 130 128 126 124 122 120 118 116 114 112 110 109

2 1 144 139 137 135 133 131 129 127 125 123 121 119 117 115 113 111 107 108

4 3 105 104

6 5 103 102

8 7 101 100

10 9 99 98

12 11 97 96

14 13 95 94

16 15 93 92

18 17 91 90

20 19 89 88

22 21 87 86

24 23 85 84

26 25 83 82

28 27 81 80

30 29 79 78

32 31 77 76

34 33 75 74

36 35 73 72

37 38 40 42 44 46 48 50 52 54 56 58 60 62 64 66 68 70 71 72

PIN MAP

	<b>Emulation Technology, Inc.</b> — VLSI and SMT ADAPTERS and ACCESSORIES —	
	2344 Walsh Avenue, Bldg. F Santa Clara, Ca 95051	TEL: (408) 982-0660 FAX: (408) 982-0664
SHEET: 1 OF 1 CHECKED: GABOR KARDOS DO NOT SCALE DRAWING	DATE: 2-22-93 DRAWN: SCOTT BARCELLOS	REVISION: ASSEMBLY DRAWING ITEM: EPP144QF10SM DESCRIPTION: EPP-144-QF10-SM

NO.	ITEM	DESCRIPTION	QTY.
1	144-QF10S	BASE BOARD	1
2	T18-FEM-50MIL	MOUNT ON FRONT OF #1	8
3	QF10-SPACER-A	MOUNT ON REAR OF #1	1